

106 年度研發替代役廠商求才登記表

公司/單位名稱：NVIDIA	統一編號或立案字號：27934172
公司/單位負責人：柏凱倫	資本額：USD 50,000
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公司/單位地址：台北市內湖區基湖路 8 號	
公司/單位網址：http://www.nvidia.com.tw/page/home.html	
公司/單位聯絡 E-mail 信箱： crystalh@nvidia.com	
<p>公司簡介：</p> <p>二十年以來，NVIDIA 始終站在電腦繪圖藝術和科學的視覺運算浪潮最前端。</p> <p>我們發明的現代視覺運算引擎 GPU，讓繪圖領域從此擴展，納入電玩遊戲、電影製片、產品設計、醫學診斷，乃至科學研究等。</p> <p>時至今日，視覺運算已逐步成為人類與科技互動的核心要素。</p> <p>主要商品/服務項目：</p> <p>與業界最具天分的專業菁英一起工作</p> <p>本公司產品涵蓋視覺運算的所有層面，從基礎發明到結合 GPU 的處理器，再到系統元件，乃至完全整合系統。我們將目標放在三大垂直市場：遊戲、專業視覺化與設計，以及高性能運算與巨量資料分析。我們針對每一垂直市場提供處理器、軟體、工具、行銷和專業知識的平台，另外更逐漸增加提供連線服務。此外也銷售元件，並授權 IP 給希冀以豐富繪圖能力打造差異化裝置的各大 OEM，充分發揮我們針對這些市場所做的創新。</p> <p>身為視覺運算的全球領導者，我們招募此領域中的最佳頭腦，藉由他們的創造力推動產業向前進，成為本公司的驅動力。因此我們的品牌激發出罕見的品牌忠誠度，並以各種獨特的個人風格表現包括插圖、刺青，甚至有粉絲將小孩命名為「NVIDIA」。</p>	
招募職缺	
全職/兼職	研發替代役
工作職稱	1. 【106 年度研發替代役】Mobile Software Embedded Engineer-嵌入式系統(台北)
學歷及科系	資工或電機相關科系碩/博士生
工作內容	<ul style="list-style-type: none"> • **僅限 106 年度研發替代役資格之役男** • 資工或電機相關科系碩/博士生 (預計 2017 年 4 月後畢業) • 英文溝通能力佳 • 精通 C/C++ 語言

	<ul style="list-style-type: none"> • 熟悉作業系統理論及 Android/Linux 作業系統運作 • 瞭解嵌入式系統硬體架構 <p>What you' ll be doing:</p> <ul style="list-style-type: none"> • The candidate will work directly with customer to provide deep technical assistance in integrating NVIDIA Tegra products into customer platforms, including platform bring up, driver porting and integration, use case support, problem solving, and feature development. • The candidate will also assist customer to achieve fully optimized design with NVIDIA product and provide customer feedback as to product feature and software enhancement. <p>Ways to stand out from the crowd:</p> <ul style="list-style-type: none"> • Experience working on embedded systems and ARM processor specific • Self managing and ability to break down complex problems in to manageable tasks • Enjoys working with internal and external customers from design to production
工作時段	9:00 am-6:00 pm
工作地點	台北市內湖區基湖路 8 號
待遇	面議，優於勞基法
工作經驗	不拘
語文條件	英文(聽/中等、說/中等、讀/中等、寫/中等)
其他條件	<p>What we need to see:</p> <ul style="list-style-type: none"> • Bachelor degree in CS, CE, EE is required • Good English language skills to work effectively with global development and support team • Experience at Linux or Android

	<ul style="list-style-type: none"> • Excellent C skills • Excellent debugging and problem solving skills <p>NVIDIA is widely considered to be one of the technology world' s most desirable employers. We have some of the most brilliant and talented people on the planet working for us. If you' re creative and autonomous, we want to hear from you!</p> <p>We are an AA/EEO/Veterans/Disabled employer</p>
<p>工作職稱</p>	<p>2. 【106 年度研發替代役】GRAPHICS FIRMWARE ENGINEER- 韌體設計 (台北)</p>
<p>學歷及科系</p>	<p>資工或電機相關科系碩/博士生</p>
<p>工作內容</p>	<ul style="list-style-type: none"> • **僅限 106 年度研發替代役資格之役男** • 資工或電機相關科系碩/博士生 (預計 2017 年 4 月後畢業) • 英文溝通能力佳 • 精通 C/C++ 語言 • 熟悉 Assembly (x86 processor or other low level) Programming • Unix based system 操作經驗 <p>What you' ll be doing:</p> <ul style="list-style-type: none"> • Development and maintenance of SW tools and FW projects for various Graphics Firmware function. • Co-work with global GFW teams and with other SW/HW teams in regards of GPU and Graphics Board projects

	<p>support.</p> <p>Ways to stand out from the crowd:</p> <ul style="list-style-type: none"> • Good communication skills in verbal and written English. • Assembly (especially for x86 processor) or other low level programming experiences. • HW control or Micro Processor programming experiences. • Unix based system operation experiences.
工作時段	9:00 am-6:00 pm
工作地點	台北市內湖區基湖路 8 號
待遇	面議，優於勞基法
工作經驗	不拘
語文條件	英文(聽/中等、說/中等、讀/中等、寫/中等)
其他條件	<p>What we need to see:</p> <ul style="list-style-type: none"> • BS or MS in CS or EE. • Familiar with computer architecture, C and C++ languages. <p>NVIDIA is widely considered to be one of the technology world' s most desirable employers. We have some of the most brilliant and talented people on the planet working for us. If you' re creative and autonomous, we want to hear from you!</p>

	We are an AA/EEO/Veterans/Disabled employer
工作職稱	3. 【106 年度研發替代役】Product Test Engineer-Wafer 測試 (HsinChu)
學歷及科系	資工或電機相關科系碩/博士生
工作內容	<ul style="list-style-type: none"> • **僅限 106 年度研發替代役資格之役男** • 電機/電子相關科系碩/博士生 (預計 2017 年 4 月後畢業) • 英文溝通能力佳 • 熟悉 Wafer Sort/封裝測試有興趣者 <p>What you' ll be doing:</p> <ul style="list-style-type: none"> • Assist product test engineer to setup and debug wafer sorting program on ATE • Sustain the wafer sort program, and collect all necessary data for program robustness or yield improving • Maintain and update testing related document • Cross yield check by statistical tool, lot disposition, and subcon management <p>What we need to see:</p> <ul style="list-style-type: none"> • BS degree in EE, CS, or semiconductor related filed. • Computer language programming is a plus • Good communication skills and command of speaking and writing in English
工作時段	9:00 am-6:00 pm
工作地點	新竹市
待遇	面議，優於勞基法
工作經驗	不拘
語文條件	英文(聽/中等、說/中等、讀/中等、寫/中等)
其他條件	<p>What we need to see:</p> <ul style="list-style-type: none"> • BS degree in EE, CS, or semiconductor related filed. • Computer language programming is a plus

	<ul style="list-style-type: none"> • Good communication skills and command of speaking and writing in English
工作職稱	4. 【106 年度研發替代役】 TEST METHODOLOGY ENGINEER (SW Developer) (新竹)
學歷及科系	無特定要求
工作內容	<p>We are now looking for a Test Methodology Engineer in our Taiwan, Hsinchu Office!</p> <p>What you' ll be doing:</p> <ul style="list-style-type: none"> • ATE test method code debugging and development • Scripting tools debugging and development • Web-based tools debugging and development • Help to solve production issues or throughput issues that might be related to test methodology <p>Ways to stand out from the crowd:</p> <ul style="list-style-type: none"> • Knowledge in scripting languages such as perl or python is a plus • Knowledge in ATE or chip/board/system/software testing is a big plus.
工作時段	9:00 am-6:00 pm
工作地點	新竹市
待遇	面議，優於勞基法
工作經驗	不拘
語文條件	英文(聽/中等、說/中等、讀/中等、寫/中等)
其他條件	<p>What we need to see:</p> <ul style="list-style-type: none"> • Master/Bachelor in Engineering or Science • Good skills in programming languages such as C/C++ or Java

	<ul style="list-style-type: none"> • Strong problem solving capability • English communication capability is required
工作職稱	5. 【106 年度研發替代役】DFT/CAD 工程師 (台北/新竹)
學歷及科系	資工或電機相關科系碩/博士生
工作內容	<ul style="list-style-type: none"> • **僅限 106 年度研發替代役資格** • 資工或電機相關科系碩/博士生 (預計 2017 年 4 月後畢業) • 熟悉 IC 設計流程 (Verilog RTL、Simulation、Timing Analysis 等相關 IC Design Flow) 熟悉 Design for Testability 技術, 包含 Scan / ATPG、Delay Test、Memory BIST、Boundary Scan、Diagnosis 等 • 對於 Nanometer / SoC DFT Implementation、開發及推廣設計流程有興趣者 <p>What you' ll be doing:</p> <ul style="list-style-type: none"> • Implement testing architecture of VLSI • Generating test patterns and chip bringup • Develop testing methodology for VLSI • Develop design flow for testing <p>What we need to see:</p> <ul style="list-style-type: none"> • Master degree of Electrical Engineering/Computer Engineering/Computer Science • Good understanding of Design for Testing including Scan/ATPG/BIST/JTAG • Familiar with Verilog • Experiences with RTL and Simulation. • Understanding of Timing is plus • Skills of C/C++/Perl/Tck is a plus
工作時段	9:00 am-6:00 pm
工作地點	台北市內湖區 / 新竹市
待遇	面議, 優於勞基法
工作經驗	不拘
語文條件	英文(聽/中等、說/中等、讀/中等、寫/中等)

其他條件	<ul style="list-style-type: none"> • 資工或電機相關科系碩士畢業生 • 熟悉 IC 設計流程 • 精通積體電路可測試性之設計與模擬 <p>Ways to stand out from the crowd:</p> <ul style="list-style-type: none"> • Strong English communication is required.
工作職稱	<p>6. 【106 年度研發替代役】Planner -物料生產企劃 (HsinChu)</p>
學歷及科系	<p>工業工程/材料工程相關科系碩/博士生</p>
工作內容	<ul style="list-style-type: none"> • **僅限 106 年度研發替代役資格之役男** • 工業工程/材料工程相關科系碩/博士生 (預計 2017 年 4 月後畢業) • 英文溝通能力佳 • 熟悉資材管理領域 (Production/Material/Factory planning)，且精通 Excel/SAP 背景尤佳 <p>What you' ll be doing:</p> <ul style="list-style-type: none"> • Support Business / Material planning team daily work. • Support Quotation, PR/PO and data maintenance (Price/Lead Time) into the system. • Support planner to Issue the work order to Subcons and Expedites operations that delay schedules and alter schedules to meet unforeseen conditions. • Monitor Supplier' s material and Wip report and review slow moving inventory / Wip, drive the disposition with Owners. • Assists planner in planning troubleshooting, enhancements and new production line and products setup. • Support Planning team to establish and maintain metrics to measure performance against scheduling, inventory, logistics, and other pertinent operations

	<p>goals.</p> <ul style="list-style-type: none"> • Monitor and analyze inventory requirements and production and/or supply plans. Adjusts plans accordingly. • Perform routine daily / weekly production reports. • Perform other related duties as assigned
工作時段	9:00 am-6:00 pm
工作地點	新竹市
待遇	面議，優於勞基法
工作經驗	不拘
語文條件	英文(聽/中等、說/中等、讀/中等、寫/中等)
其他條件	<p>What we need to see:</p> <ul style="list-style-type: none"> • Major in IE, IEM and Business Administration preferred. • Fluency in English is preferred. • Expertise in MS PowerPoint/Excel; SAP experience is a plus (excel VBA is also a plus). • Requires strong verbal / written communication skills, good teamwork and comfortable with workplace stress. • Organized, self-starter with a strong sense of ownership is a must. • Able to handle multiple, competing priorities efficiently and effectively.

	<ul style="list-style-type: none"> • Strong analytical, quantitative and problem solving skills . • Good presentation skills (both English and Chinese). • Basic knowledge of semiconductor manufacturing flow and processes is a plus.
工作職稱	7. 【106 年度研發替代役】Product Engineer-半導體製程 (HsinChu)
學歷及科系	電機/電子相關科系碩/博士生
工作內容	<ul style="list-style-type: none"> • **僅限 106 年度研發替代役資格之役男** • 電機/電子相關科系碩/博士生 (預計 2017 年 4 月後畢業) • 英文溝通能力佳 • 熟悉半導體製程領域並對良率分系及改善有強烈興趣者 <p>What you' ll be doing:</p> <ul style="list-style-type: none"> • Analyze/improve yield from NPI bring-up to sustaining devices, work with related teams to solve engineering related issues. • Analyzing, troubleshooting and recommending solutions to optimize the product performance. • Analyzing Board yield and FA • Reducing cost and DPPM implementations. <p>What we need to see:</p> <ul style="list-style-type: none"> • MS major in Electrical or other related fields • Good knowledge of semiconductor manufacturing or board processes, and established, proven program management capability and experience and results. • Must be a strong team player, supporting & analytical skill and good working habits • Self-starter who can accomplish assigned tasks with minimal supervision.
工作時段	9:00 am-6:00 pm

工作地點	新竹市
待遇	面議，優於勞基法
工作經驗	不拘
語文條件	英文(聽/中等、說/中等、讀/中等、寫/中等)
其他條件	<p>What we need to see:</p> <ul style="list-style-type: none"> • MS major in Electrical or other related fields • Good knowledge of semiconductor manufacturing or board processes, and established, proven program management capability and experience and results. • Must be a strong team player, supporting & analytical skill and good working habits • Self-starter who can accomplish assigned tasks with minimal supervision.
應徵截止日期	105/12
應徵方式	<p>請務必利用下列格式回覆相關求職資訊: TW-Recruitment@nvidia.com 我們將有專人與您聯繫！</p> <ul style="list-style-type: none"> • 投遞職缺名稱: (如超過一個職缺，請詳列排序並寫上所有職缺全名) • 中文姓名: • 英文姓名: • 學校 及 科系名稱: • 聯絡手機: • 個人 Email 信箱: • 英文履歷表及相關在校成績證明: (履歷表無限定格式,煩請列出專長,熟悉領域及相關經驗)